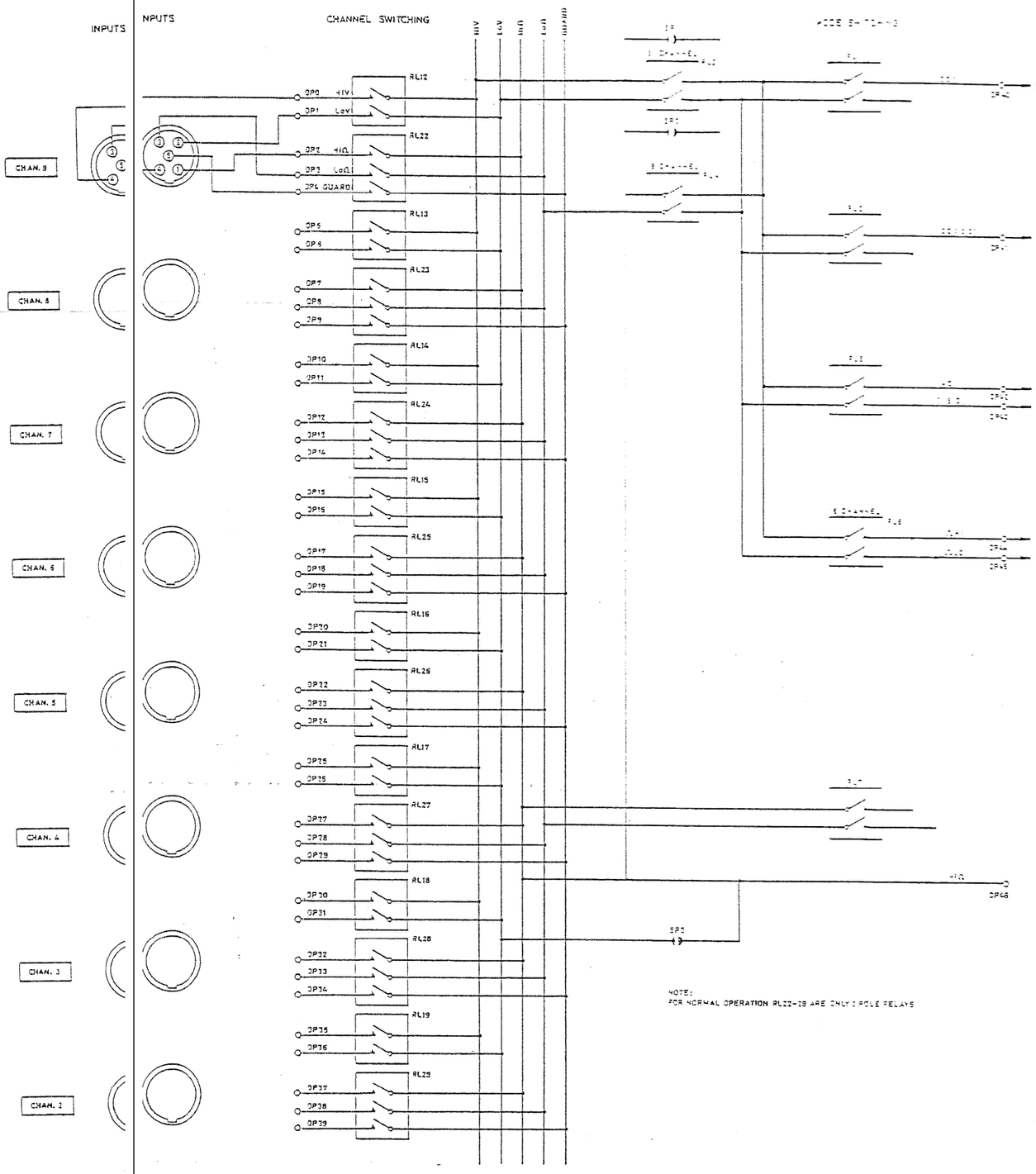
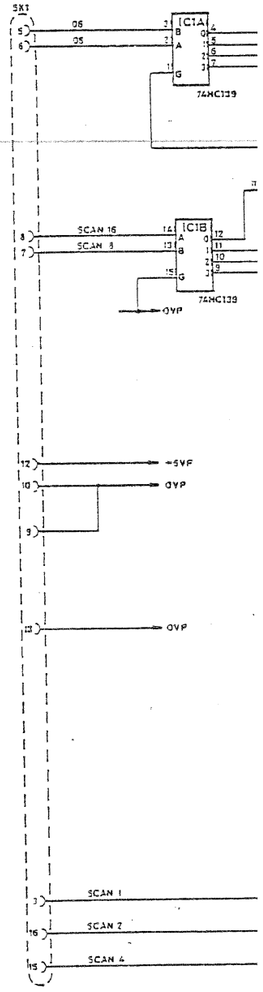


Figure 5.5 *PCB 7, Sheet 1 of 2, Scanner Option Circuit Diagram 70617507, Issue F*



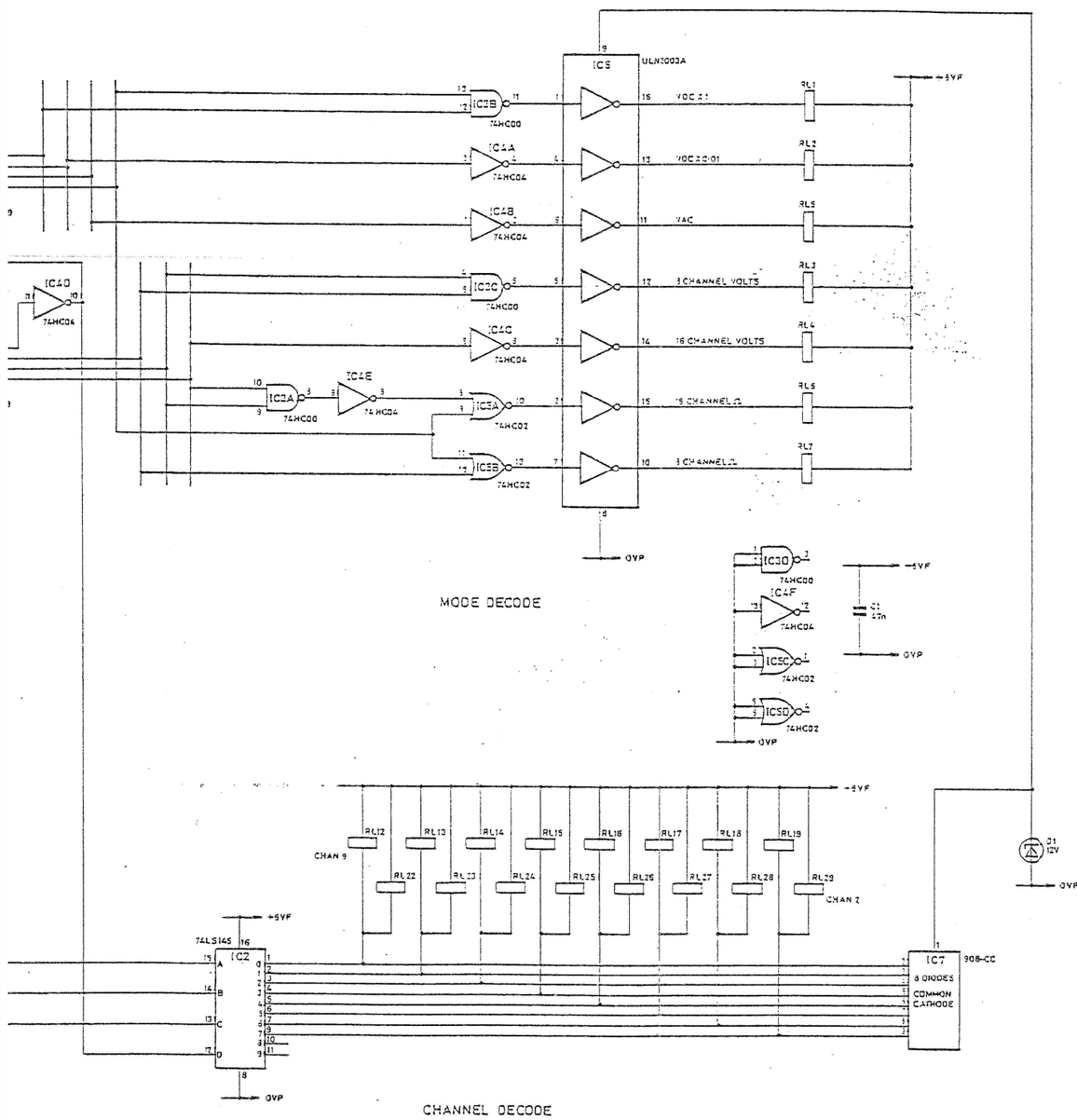
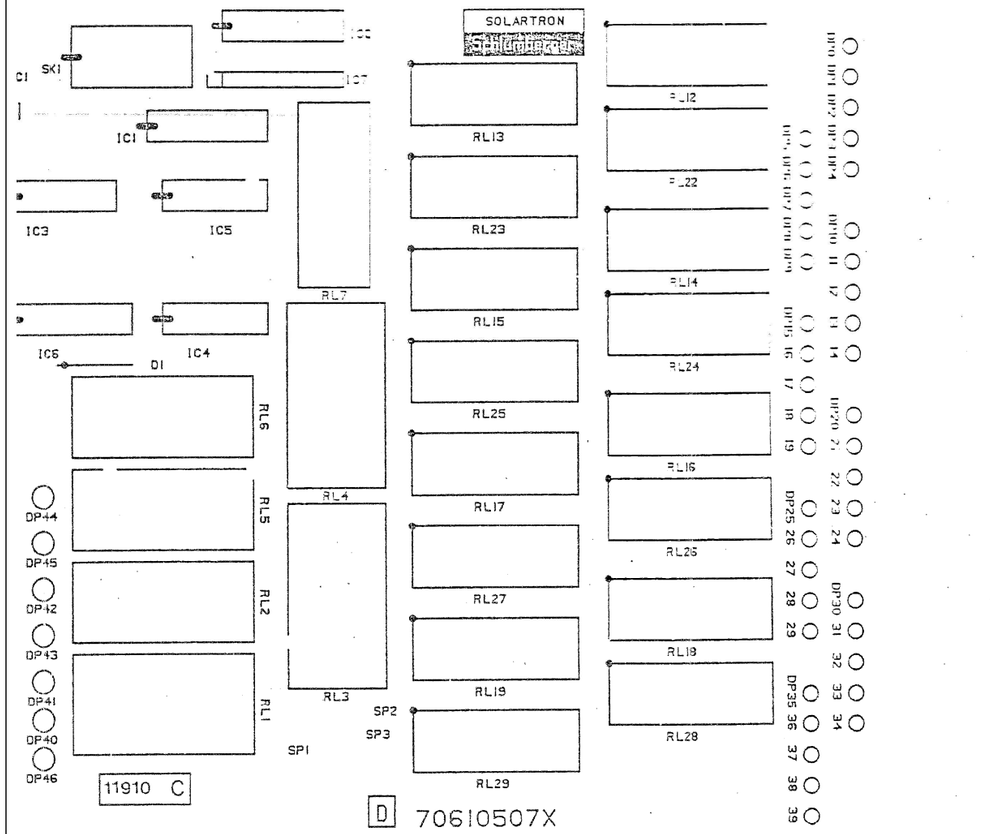


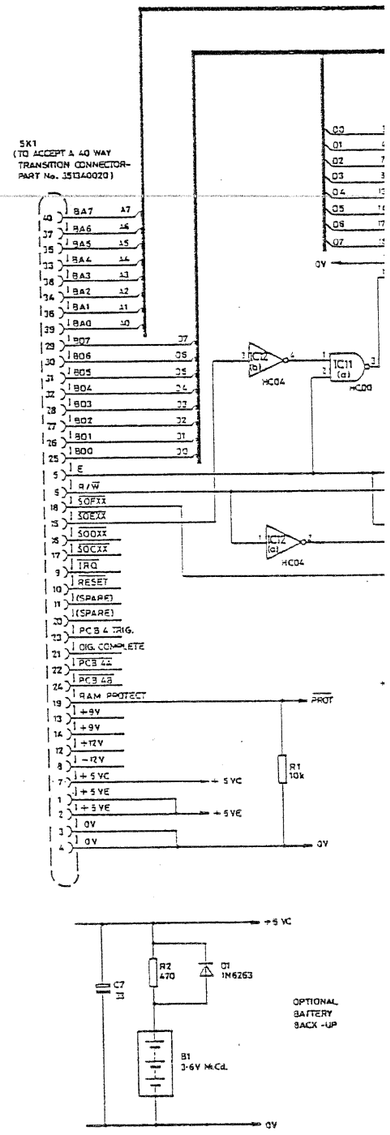
Figure 5.5 *PCB 7, Sheet 2 of 2, Scanner Option Circuit Diagram 70617507, Issue F*

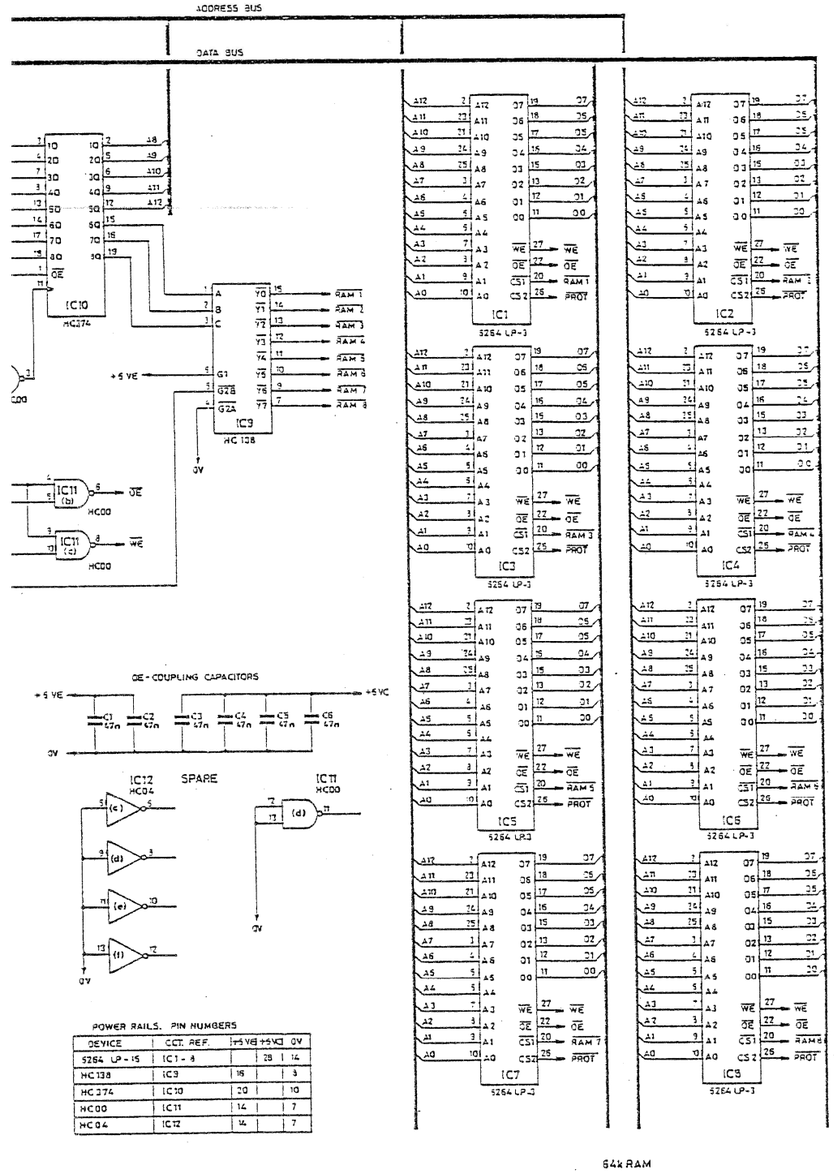


07-D SHT 4 NOTATION

70619

Figure 5.6 *PCB 7 Scanner Option, Component Layout 70610507, Issue C*





64k RAM

Figure 5.7 *PCB 2 Memory Expansion Option Circuit Diagram 70617502, Issue B*

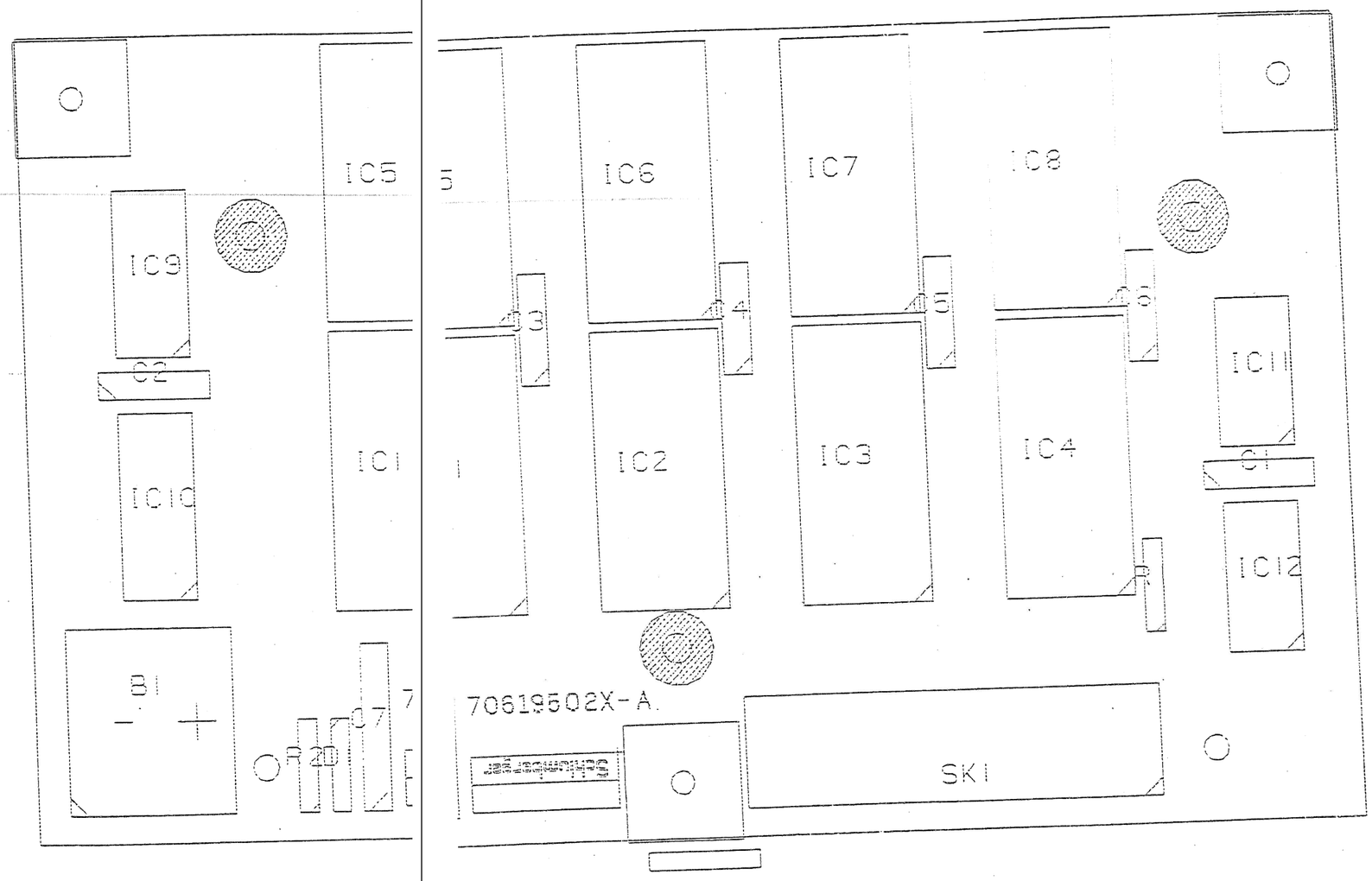
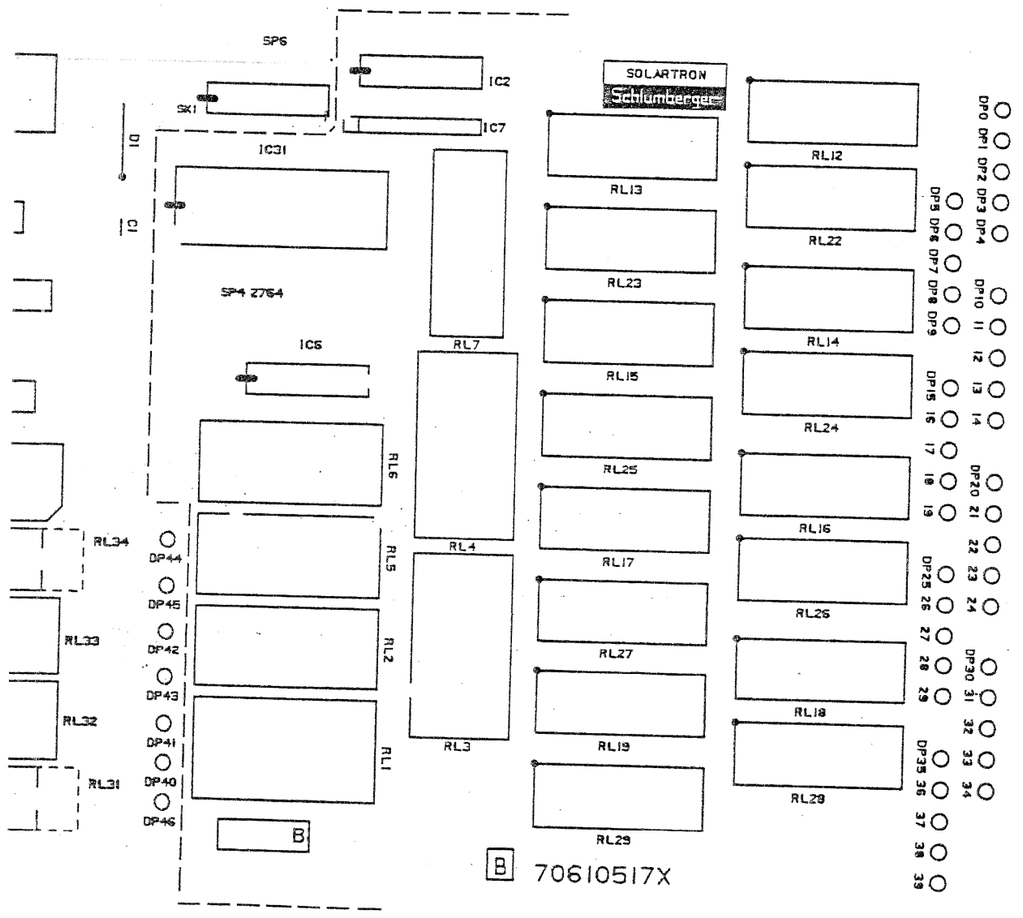
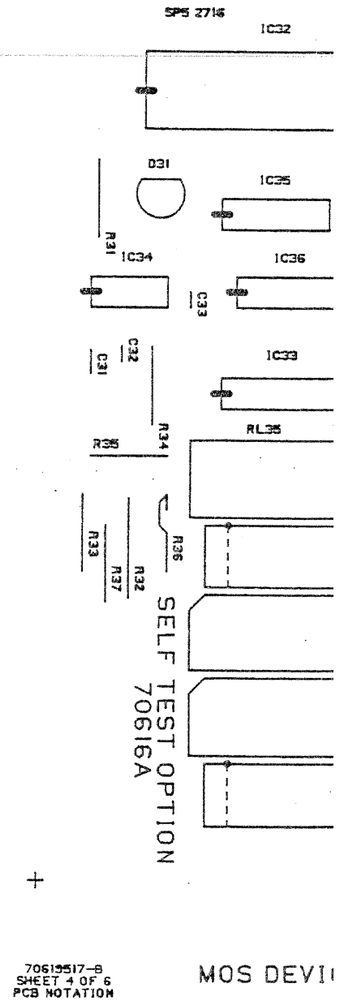


Figure 5.8 *PCB 2 Memory Expansion Option Component Layou*
70610502, Issue A

Note. Components to the right of the broken outline are currently not fitted to Pcb 17



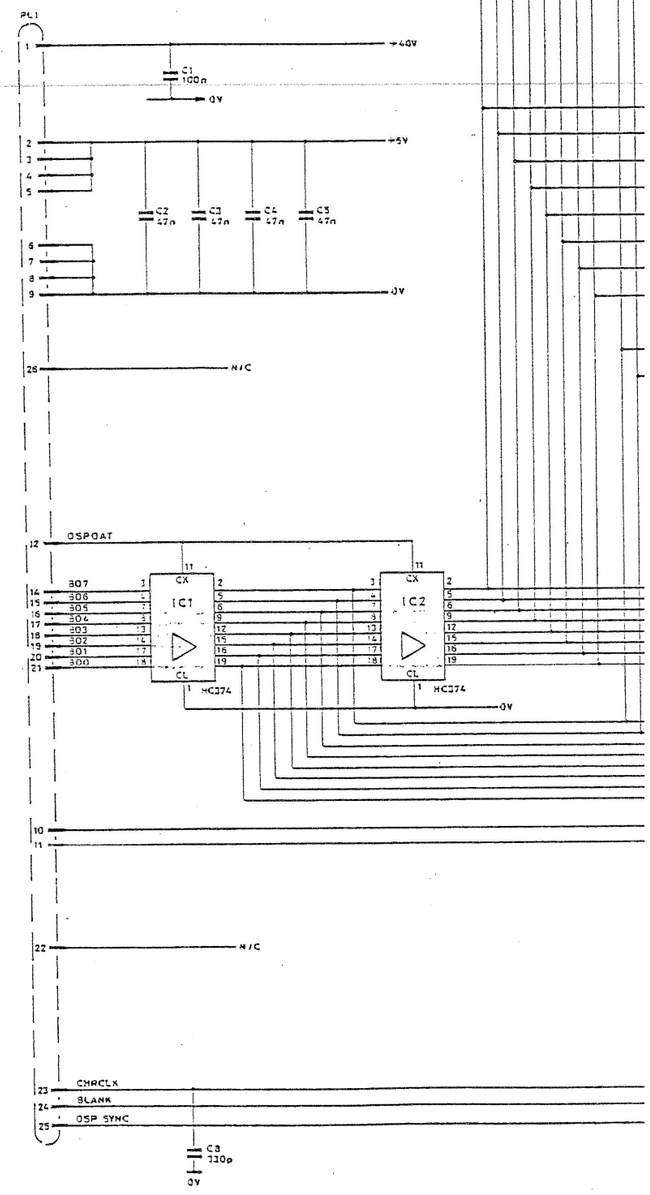


70619517-B
SHEET 4 OF 6
PCB NOTATION

MOS DEVI

CES ARE USED ON THIS BOARD,SILK SCREEN WITH YELLOW PAINT.

Figure 5.9 *PCB 17 Self Test Option Component Layout 7061, Issue B*



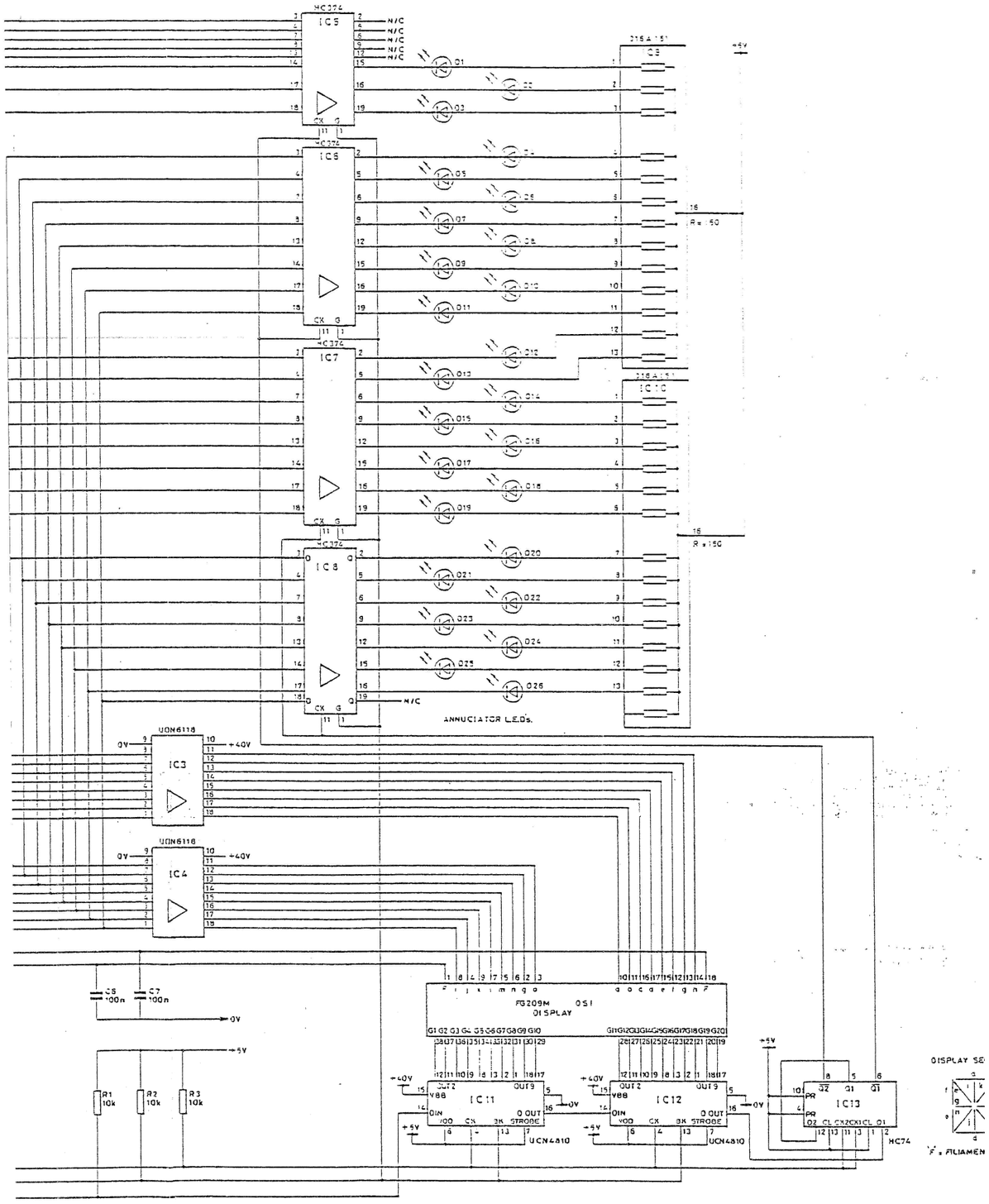
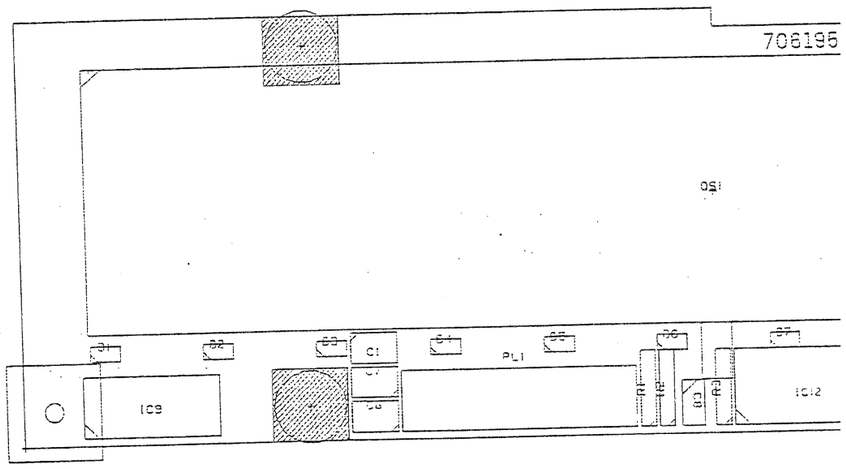


Figure 6.1. *PCB 1, Display.*
Circuit Diagram 70617501, Issue C.



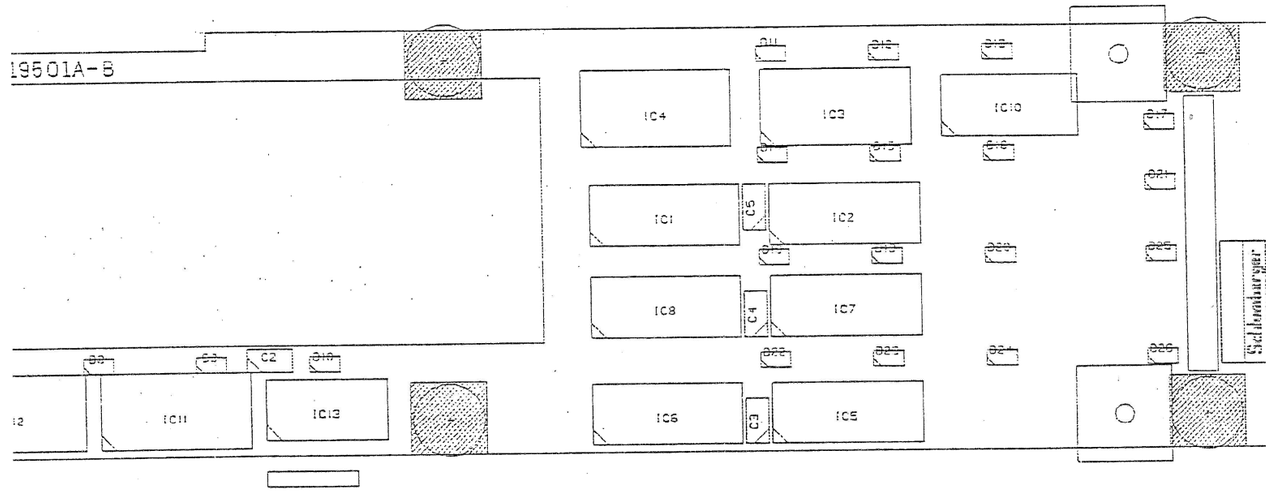
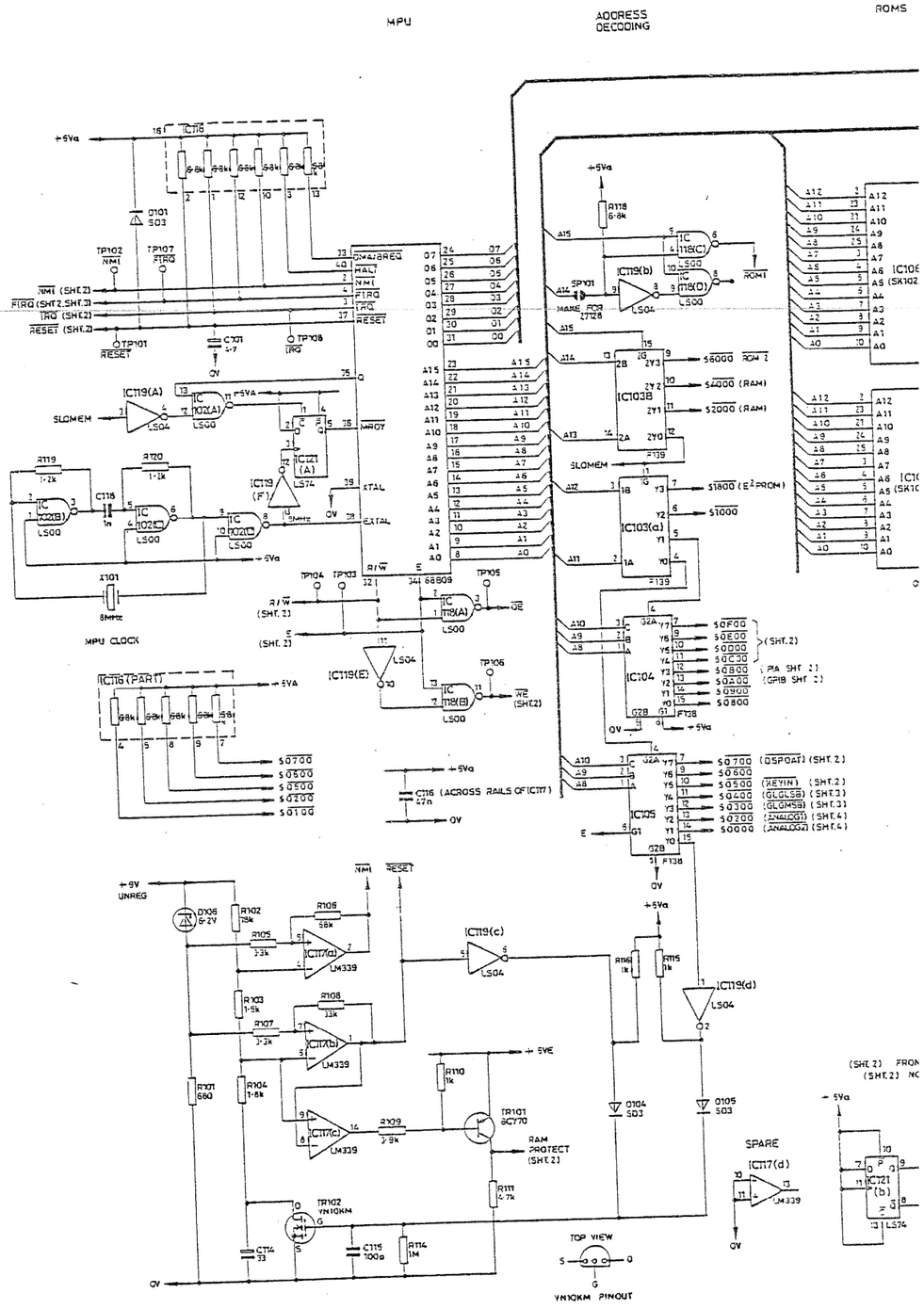


Figure 6.2. *PCB 1, Display, Component Layout. Diagram 70610501, Issue B.*



CCT REF	DEVICE	+5Va	+5VC	GNDOV	-5VE
IC101	68B09	1		1	
IC103	F139	16		8	
IC104, 105	F138	16		8	
IC106, 7	27C255	28		14	
IC110–112	6264LP15		28	14	
IC113	2816A	28		14	
IC114	LS245			10	20
IC115	LS244	20		10	
IC117	LM339	3		12	
IC118	LS00	14		7	
IC119	LS04	14		7	
IC107	27128	28		14	
IC102	LS00	14		7	
IC121	LS74	14		7	

Figure 6.3. *PCB 3, Sheet 1 of 5, MPU & Memory. Circuit Diagram 70617503, Issue L.*

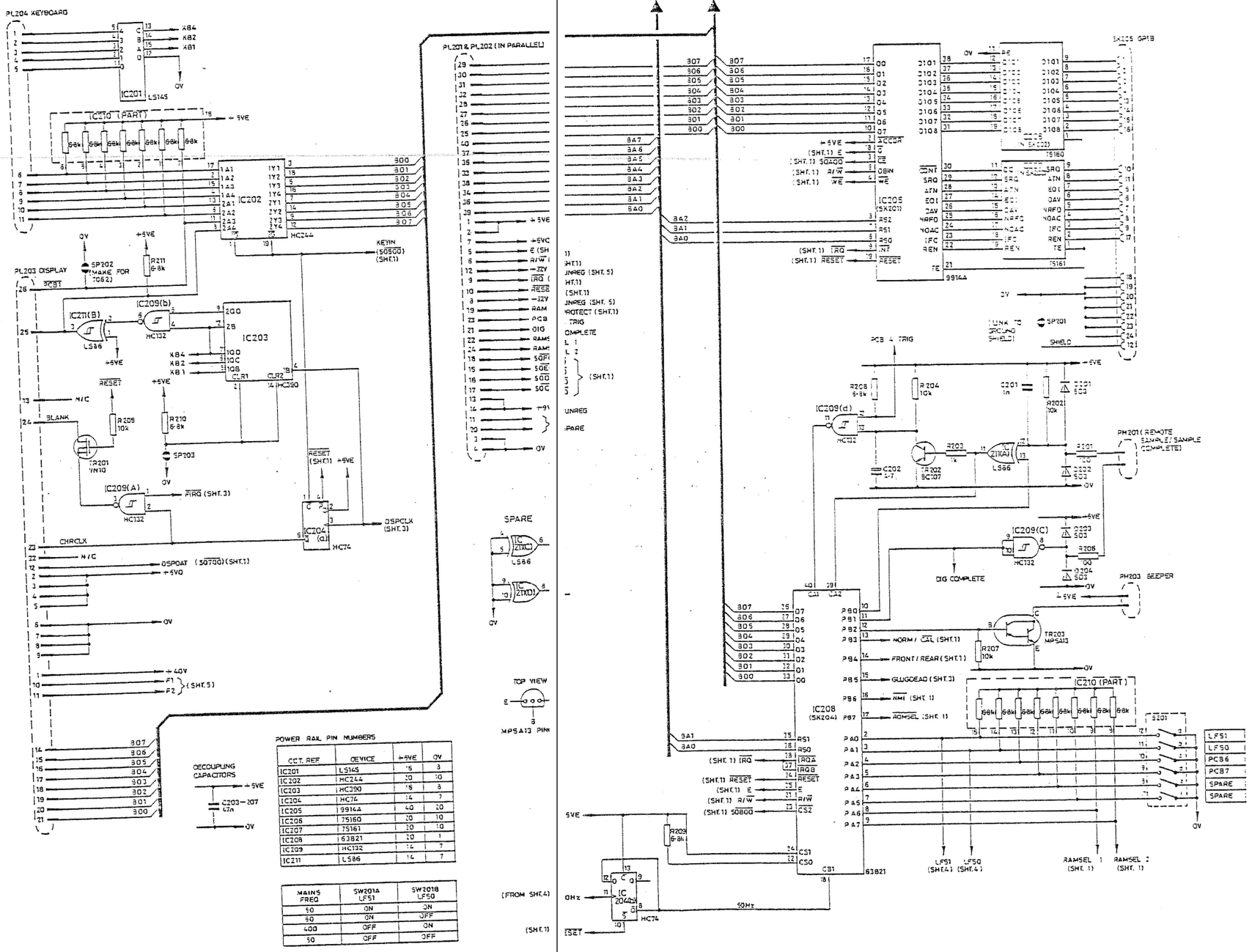
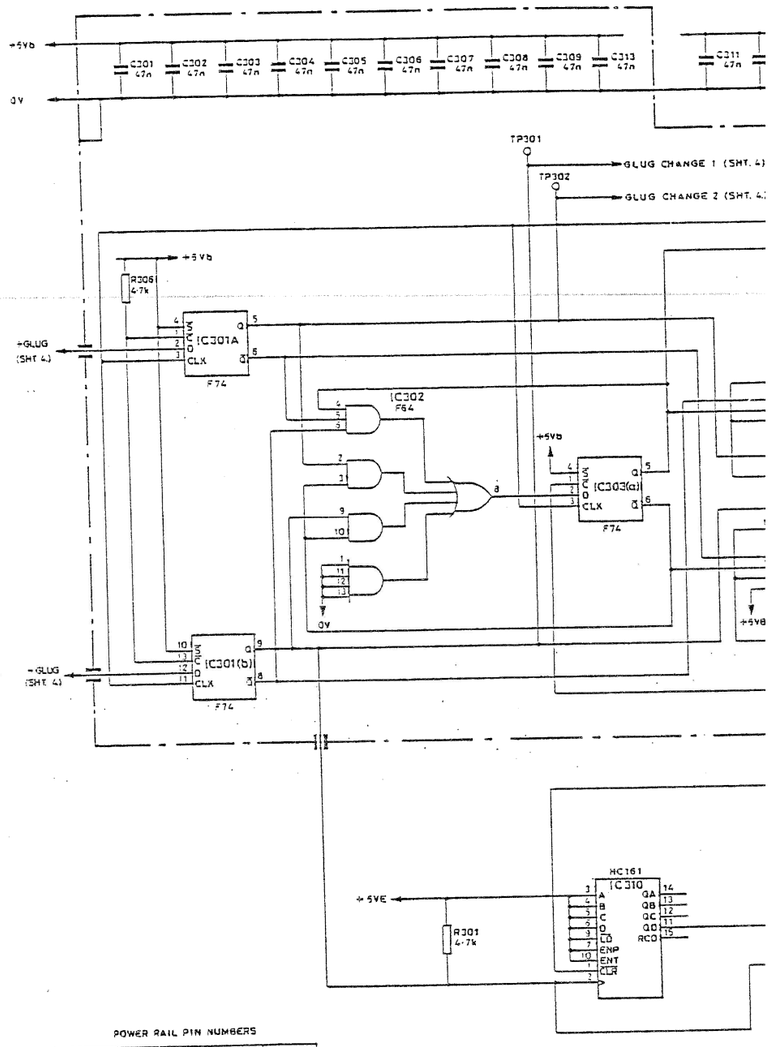


Figure 6.3. *PCB 3, Sheet 2 of 5, General Input/Output. Circuit Diagram 70617503, Issue F.*



CCT REF.	DEVICE	+5Vb	GND	+5VE
IC301	F74	14	7	
IC302	F64	14	7	
IC303	F74	14	7	
IC304	F64	14	7	
IC305	F161	16	8	
IC306	LS161	16	8	
IC307	LS393	14	7	
IC308	LS374	20	10	
IC309	LS374	20	10	
IC310	HC161		8	16
IC312	HC74		7	14
IC313	F164	14	7	
IC314	HC00		7	14
IC315	HC174		8	16

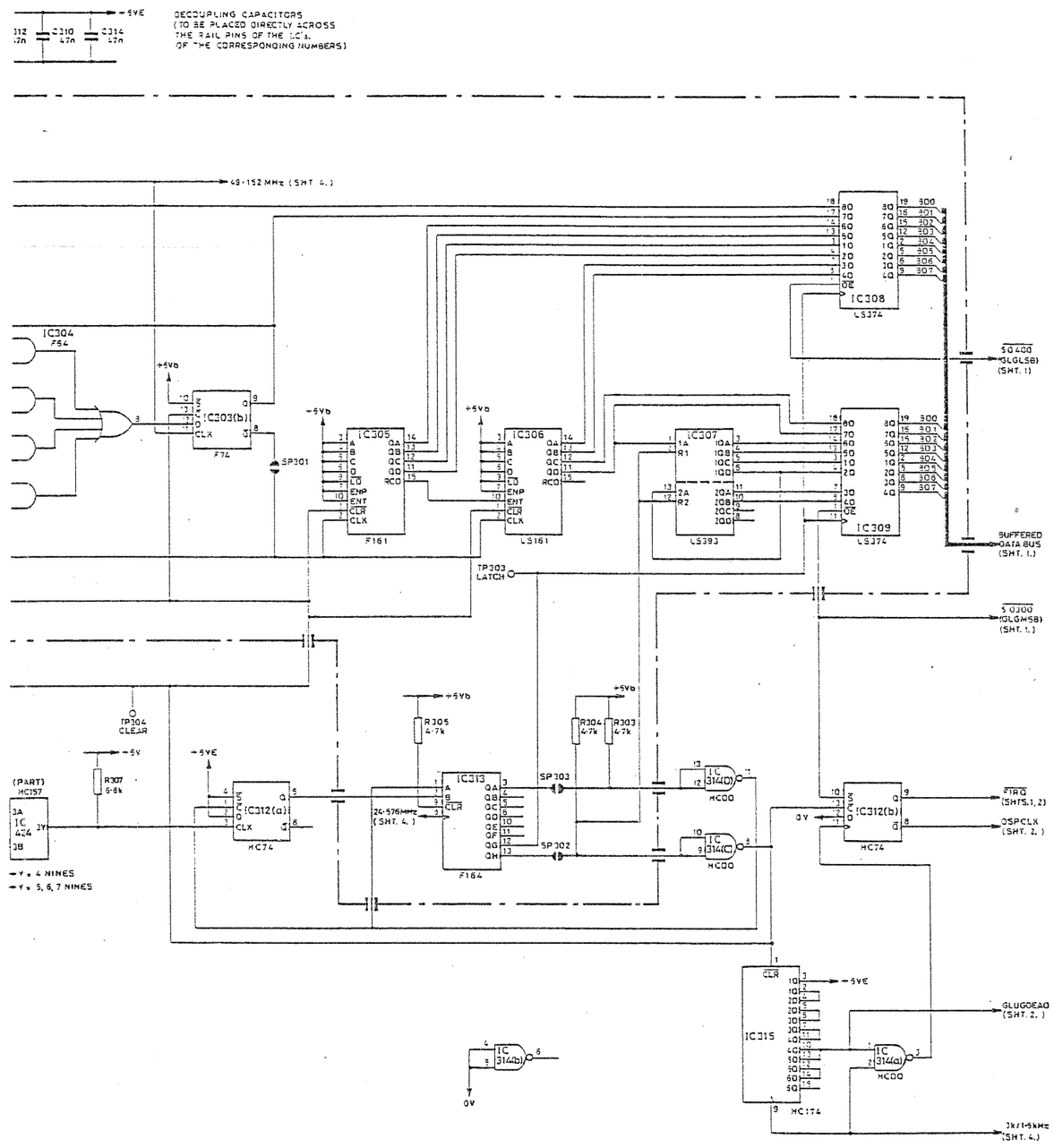


Figure 6.3. *PCB 3, Sheet 3 of 5, Glug Counters & Latches. Circuit Diagram 70617503, Issue D.*