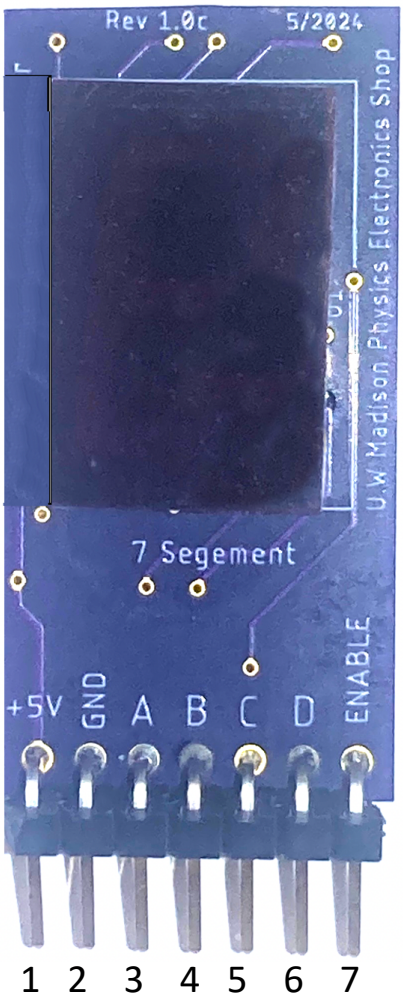
SOLID-STATE HEXADECIMAL DISPLAY WITH
INTEGRAL TTL CIRCUIT TO ACCEPT, STORE,
AND DISPLAY 4-BIT BINARY DATA



PIN 1 SUPPLY VOLTAGE

PIN 2 COMMON GROUND

PIN 3 LATCH DATA INPUT A

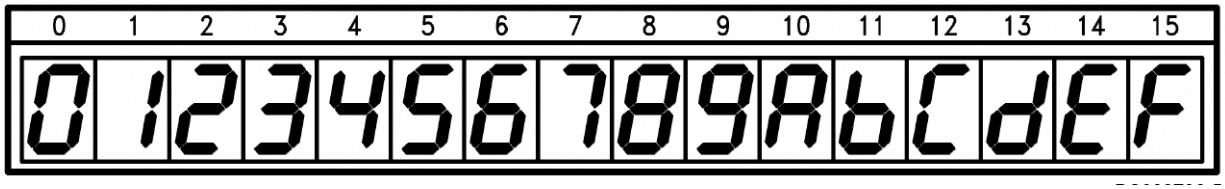
PIN 4 LATCH DATA INPUT B

PIN 5 LATCH DATA INPUT C

PIN 6 LATCH DATA INPUT D

PIN 7 LATCH ENABLE INPUT

CHARACTER DISPLAY FOR DATA LATCH CONTENT



TIL311 SUBSTITUTE
HEXADECIMAL DISPLAY WITH LOGIC

Description:

This hexadecimal display contains a four-bit latch, decoder, driver, and a seven-segment light-emitting-diode (LED) character display 7-pin package. A description of the input functions of this device follows:

FUNCTION	PIN #	DESCRIPTION
SUPPLY VOLTAGE V_{CC}	1	+5 V Supply (MIN 4.5 V, MAX 5.5 V)
COMMON GROUND	2	Power Supply Return
LATCH DATA INPUT A	3	Data 2^0 bit (lsb)
LATCH DATA INPUT B	4	Data 2^1 bit
LATCH DATA INPUT C	5	Data 2^2 bit
LATCH DATA INPUT D	6	Data 2^3 bit (msb)
LATCH DATA INPUT ENABLE	7	HIGH to write to latch, LOW to hold (independent of inputs)